

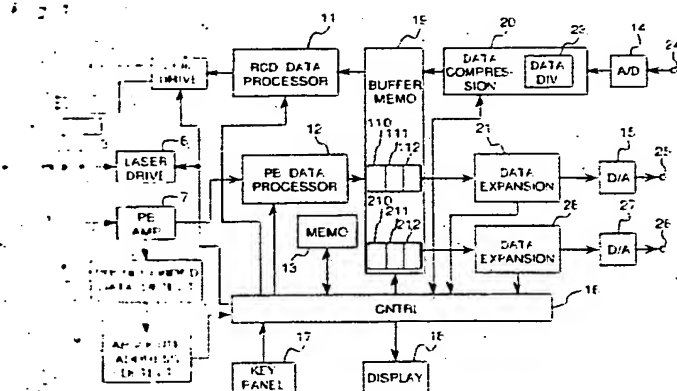


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from the disk during data reproduction. Using the data expansion circuit to expand the compressed data one block at a time, the data recording and reproducing apparatus can reproduce the original continuous digital data stream. Plural continuous digital data selections can thus be reproduced by alternately fetching data for the plural selections from the disk in compressed data block units and temporarily storing the data to a buffer memory for expansion.



## BACKGROUND OF THE INVENTION

### 1. Field of the invention

The present invention relates to an optical disk recording and reproducing apparatus using a magneto-optical disk and, more particularly, to a data recording and reproducing apparatus, such as a Compact Disc player.

### 2. Description of the prior art

Fig. 9 is a block diagram of a conventional apparatus used to record and reproduce continuous digital data, specifically digital audio data.

To make a recording, analog audio data is input from the input terminal 24 and converted to digital audio data by the A/D converter 14. The converted data is then converted by the recording data processing circuit 11 to the data format required for recording, and the processed data is applied to the coil 4 by the coil driver 5. A magnetic field modulated according to the recording data is then applied to the disk 1 by the coil 4.

Based on the commands from the controller 16, the laser driver 6 causes the optical pickup 3 to emit a high output optical beam to the disk 1. This causes a local temperature rise enabling the data to be recorded.

Absolute disk addresses are prerecorded to the disk 1. This address data read from the disk 1 by the optical pickup 3 is input through the playback amplifier 7 to the prerecorded data detection circuit 8, and a subset of the data is input to the CLV control circuit 10.

The CLV control circuit 10 applies constant linear velocity (CLV) control based on the input data to keep the spindle motor 2 driving at a constant linear velocity. The data from the prerecorded data detection circuit 8 is also input to the absolute address detection circuit 9, and the location of the optical pickup 3 is input to the controller 16. When data is recorded to the disk 1, the absolute address of the disk area to which data is recorded is registered in and managed by the table of contents (TOC) memory 13.

When data is reproduced, the area to which the data to be reproduced is recorded (i.e., the address) is fetched, and the optical pickup 3 accesses that area. The data recorded to the disk 1 is then read by the optical pickup 3, and input through the playback amplifier 7 to the playback data processing circuit 12.

The playback data processing circuit 12 then converts the input data to digital audio data, and inputs the digital audio data to the D/A converter 15. The D/A converter 15 converts the digital audio data to analog audio data, and outputs the analog

audio data from the output terminal 25.

The data record/reproduce commands are input from the operating key panel 17 to the controller 16, which generates the corresponding control signals. The display 18 is controlled by the controller 16 to display the current optical pickup 3 location and the operating status.

With this data recording and reproducing apparatus, however, it is impossible to simultaneously reproduce different, plural selections because reproduction of a recorded selection is executed in real time with reading of the data from the disk by the optical pickup. In addition, when non-consecutive selections at different disk addresses are alternately reproduced, there is a momentary pause in playback due to the seek time of the optical pickup disk access operation, and it is not possible to instantaneously change selections and maintain uninterrupted playback.

## SUMMARY OF THE INVENTION

Therefore, an object of the present invention is to provide a data recording and reproducing apparatus or data reproducing apparatus enabling smooth, uninterrupted reproduction of plural data selections.

To achieve this object, a data recording and reproducing apparatus or a data reproducing apparatus for reproducing recorded data from a recording medium to which continuous digital data is divided into plural blocks and compressed for recording according to the invention comprises a buffer memory. This device is characterized by reading non-continuous data in compressed data block units from the recording medium, temporarily storing the data blocks in the buffer memory, and sequentially inputting the data in block units from the buffer memory to the data expansion circuit to reproduce the discrete data blocks as continuous digital data, thus simultaneously reproducing plural data blocks.

When a previously recorded selection is reproduced using the apparatus according to the present invention, the data reading and reproducing operations do not need to be executed in real time, and it is therefore possible to reproduce different selections simultaneously.

For example, let us assume that a 4-second digital audio data block has been compressed and stored to disk as a 1-second data block. It only takes the optical pickup 1 second to read the data from the disk, but the 4-second source signal can be reproduced using the data expansion circuit. This leaves the optical pickup idle for 3 seconds, and this idle time can be used by the optical pickup to access, read, and buffer a different selection.

## BRIEF DESCRIPTION OF THE DRAWINGS

The present invention will become more fully understood from the detailed description given below and the accompanying diagrams wherein:

Fig. 1 is a block diagram of a data recording and reproducing apparatus according to the first embodiment of the invention,

Fig. 2 is a graph of the time-based data flow during the recording process,

Fig. 3 is a graph of the time-based movement of the optical pickup relative to the absolute disk address during recording,

Fig. 4 is a graph of the time-based data flow during the reproducing process,

Fig. 5 is a graph of the time-based movement of the optical pickup relative to the absolute disk address during the reproducing process,

Fig. 6 is a block diagram of a data recording and reproducing apparatus according to the second embodiment of the invention,

Figs. 7a and 7b taken together as shown in Fig. 7 is a graph of the time-based data flow during the data reproducing process,

Fig. 8 is a graph of the time-based movement of the optical pickup relative to the absolute disk address during the data reproducing process, and

Fig. 9 is a block diagram of a conventional data recording and reproducing apparatus.

## DESCRIPTION OF PREFERRED EMBODIMENTS

The preferred embodiments of the present invention are described hereinbelow with reference to the accompanying figures, of which Fig. 1 is a block diagram of the first embodiment.

The data recording and reproducing apparatus according to the first embodiment has, in addition to the elements provided in the prior art apparatus shown in Fig. 9, buffer memory 19, data compression circuit 20, data divider 23 included in the data compression circuit 20 and two data expansion circuits 21 and 28.

The steps for recording data of selection A (such as a music) in a particular area starting from the absolute address 10'00'00 are first described. The time-based data flow during the recording process is shown in Fig. 2, and the time-based movement of the optical pickup relative to the absolute disk address during recording is shown in Fig. 3.

The recording operation of the first embodiment is described below with reference to Figs. 1 - 3.

The analog audio data to be recorded is input from the input terminal 24 and converted to digital audio data by the A/D converter 14. The converted digital audio data is compressed by the data com-

pression circuit 20 and is divided into blocks by data divider 23 after a predetermined length of data is compressed. The compression ratio assumed in Fig. 2 is 1/4.

The digital audio data is compressed in predetermined unit amounts A0 100, A1 101, A2 102, A3 103 ..... An, resulting in the corresponding compressed data blocks a0 110, a1 111, a2 112, a3 113 ..... an. The compressed data blocks a0 110, a1 111, a2 112, a3 113 ..... an are temporarily stored to the buffer memory 19, and fetched therefrom for conversion by the recording data processing circuit 11 to the recording data format, resulting in data blocks a'0 120, a'1 121, a'2 122, a'3 123 ..... a'n.

In the example shown in Figs. 2 and 3 three data blocks are recorded together.

At time t1 the first digital audio data block A0 100 is input to the data compression circuit 20 and compression begins. As shown in Fig. 3, the optical pickup 3 begins to access absolute address 10'00'00, which is the recording start position.

At time t2, while the seek operating continues, input of digital audio data block A0 100 is completed, and compressed data a0 110 is generated and stored to the buffer memory 19. Input of the next digital audio data block A1 101 to the data compression circuit 20 then begins.

At time t3, input of digital audio data block A1 101 is completed, the data is compressed, and the compressed data a1 111 is stored to the buffer memory 19. At this point two compressed data blocks (a0 and a1) are stored in the buffer memory 19.

Input of the next digital audio data block A2 102 to the data compression circuit 20 begins at time t3 and ends at time t4, at which point the compressed data a2 112 is generated and stored in the buffer memory 19. Three compressed data blocks (a0, a1, a2) are thus stored in the buffer memory 19.

These three compressed data blocks (a0, a1, a2) are input to the recording data processing circuit 11, whereby the data is converted to recording data a'0 120, a'1 121, a'2 122 and input to the coil driver 5. Input of the next digital audio data block A3 103 also begins from time t4.

At time t5, the optical pickup 3 completes the seek operation and starts to record the recording data a'0 120, a'1 121, a'2 122.

Recording is completed at time t6, and the optical pickup 3 enters a standby (still) mode until the next recording operation begins at time t9. Input of digital audio data block A3 103 is also completed, and compressed data a3 113 is generated and stored to the buffer memory 19.

This sequence is repeated to compress digital audio data blocks A4 104 and A5 105 to com-

pressed data a4 114 and a5 115, which are stored to the buffer memory 19. At time t9, three compressed data blocks (a3 113, a4 113, a5 115) are stored in the buffer memory 19. The data blocks are therefore input to the recording data processing circuit 11 for conversion to recording data a'3 123, a'4 124, a'5 125 and recorded by the optical pickup 3 as described above to absolute address 10'01'00.

Recording data is thus compressed and recorded simultaneously to the disk 1, and the absolute address of the area to which the selection A was recorded is registered in the TOC memory 13.

Next, the steps for reproducing simultaneously two selections A and B (such as two different music numbers) are described. It is noted that selection A is stored in an area starting from the absolute address of 10'00'00 and selection B is stored in an area starting from the absolute address of 15'00'00 in the disk 1. The time-based data flow during the reproduction process is shown in Fig. 4, and the time-based movement of the optical pickup 3 relative to the absolute disk address during the reproduction process is shown in Fig. 5.

The operation of the first embodiment during reproduction of two selections A and B is described below with reference to Figs. 1, 4 and 5. The two selections (A and B) to be reproduced are selected from the operating key panel 17, which forwards these selections to the controller 16.

The controller 16 first retrieves the location (the absolute address) of the recording areas of A and B from the TOC memory 13. The optical pickup 3 then accesses absolute address 10'00'00 as shown in Fig. 5 based on this information, and at time t1 it begins to read the recorded data a0 120, a'1 121, and a'2 122 for selection A from the disk 1.

The recorded data a0 120, a'1 121, and a'2 122 is then input through the playback amplifier 7 to the playback data processing circuit 12, and re-converted thereby to compressed data a0 110, a1 111, and a2 112. The compressed data a0 110, a1 111, and a2 112 is then stored in the buffer memory 19.

By time t2 the optical pickup 3 has finished reading the data, and therefore begins to access absolute address 15'00'00 where selection B is recorded.

By time t3 the optical pickup 3 has accessed the address, and therefore begins to read the recorded data b'0 220, b'1 221, and b'2 222 for selection B.

The recorded data b'0 220, b'1 221, and b'2 222 is likewise input through the playback amplifier 7 to the playback data processing circuit 12, and re-converted to compressed data b0 210, b1 211, and b2 212. The compressed data b0 210, b1 211,

and b2 212 is then stored in the buffer memory 19.

At time t4 during this playback operation, i.e., when compressed data b0 210 is stored to the buffer memory 19, the compressed data a0 110 and b0 210 in the buffer memory 19 are output to the respective expansion circuits 21 and 28 for expansion to the digital audio data A0 100 and B0 200. The expanded digital audio data blocks A0 100 and B0 200 are then output to the respective D/A converters 15 and 27 for conversion to the analog audio data output from the output terminals 25 and 26.

At time t5 the optical pickup 3 has finished reading the first selection B data (b'0, b'1, b'2), and therefore starts to access the absolute address 10'01'00 of the next data selection A.

At time t6, while the optical pickup 3 is still seeking the address, the next batch of compressed data a1 111 and b1 211 in the buffer memory 19 is output to the respective expansion circuits 21 and 28 for restoration to the digital audio data A1 101 and B1 201. The restored digital audio data A1 101 and B1 201 is then output to the respective D/A converters 15 and 27 for conversion to the analog audio data output simultaneously from the output terminals 25 and 26.

By time t7 the optical pickup 3 has accessed the address, and therefore begins to read the next data block (a'3 123) for selection A. The recorded data a'3 123 is input through the playback amplifier 7 to the playback data processing circuit 22, and re-converted to compressed data a3 113, which is then stored in the buffer memory 19.

By time t8 the optical pickup 3 has finished reading the data, and therefore begins to access absolute address 15'01'00 for the next data selection B.

When access is completed at time t9, the compressed data a2 112 and b2 212 in the buffer memory 19 is output to the respective expansion circuits 21 and 28 for expansion to the digital audio data A2 102 and B2 202. The expanded digital audio data A2 102 and B2 202 is then output to the respective D/A converters 15 and 27 for conversion to the analog audio data output from the output terminals 25 and 26.

During this time, however, the optical pickup 3 reads two blocks of data (b'3 223 and b'4 224) from absolute address 15'01'00 for selection B. These data blocks b'3 223 and b'4 224 are output to the playback data processing circuit 12 through the playback amplifier 7, converted to compressed data b3 213 and b4 214, and then stored to the buffer memory 19.

Reading ends at time t10, and the optical pickup 3 begins seeking the absolute address 10'01'25 of the next selection A data. This procedure is continued to simultaneously play back se-

lections A and B. By thus simultaneously reproducing two different selections by means of a single data recording and reproducing apparatus as shown in Fig. 1, functions that could not be achieved using the prior art become possible.

A second embodiment of a data recording and reproducing apparatus according to the present invention is described below with reference to Figs. 6, 7, and 8. Fig. 6 is a block diagram of the second embodiment, Figs. 7a and 7b taken together as shown in Fig. 7 is a graph of the time-based data flow during the playback process, and Fig. 8 is a graph of the time-based movement of the optical pickup relative to the absolute disk address during playback.

According to the second embodiment, when compared with the first embodiment of Fig. 1, data expansion circuit 28 and D/A converter 27 are removed. Specifically, the second embodiment comprises only one data expansion circuit 21 and only one D/A converter 15. A switch SW is provided in association with buffer memory 19. The switch SW can be changed in response to the manual key operation effected on the operation key panel 17. Therefore, this embodiment sequentially reproduces plural selections by switching between the selections.

The operation of this embodiment during the recording process is the same as that of the first embodiment described above, and therefore the description thereof is omitted.

The two selections (A and B) to be reproduced are selected from the operating key panel 17, which forwards these selections to the controller 16.

The controller 16 first fetches the position (the absolute address) of the recording areas of A and B from the TOC memory 13. The optical pickup 3 then accesses absolute address 10'00'00 based on this information, and at time t1 begins reading the recorded data a'0 120, a'1 121, and a'2 122 for selection A from the disk 1.

The read recorded data a'0 120, a'1 121, and a'2 122 is input through the playback amplifier 7 to the playback data processing circuit 12, converted thereby to compressed data a0 110, a1 111, and a2 112, and then stored to the buffer memory 19.

By time t2 the optical pickup 3 has finished reading the data, and therefore begins to access absolute address 15'00'00 where selection B is recorded. By time t3 the optical pickup 3 has accessed the address, and therefore begins to read the selection B recorded data b'0 220, b'1 221, and b'2 222.

The read recorded data b'0 220, b'1 221, and b'2 222 is input through the playback amplifier 7 to the playback data processing circuit 12 and converted thereby to the compressed data b0 210, b1

211, and b2 212, which is then stored in the buffer memory 19.

At time t4 when compressed data b0 210 is stored to the buffer memory 19, the controller 16 recognizes a selection A specification from the selection specification data. The controller 16 therefore causes the compressed data a0 110 to be output from the buffer memory 19 to the expansion circuit 21, which expands the compressed data a0 110 to the digital audio data A0 100. The digital audio data A0 100 is then output to the D/A converter 15, converted to analog audio data, and the analog audio data is output from the output terminal 25.

Note that the selection specification data is input from the operating key panel 17.

At time t5 the optical pickup 3 has finished reading and therefore starts to access the absolute address 10'01'00 of the next selection A data a'3 123. At time t6, while the optical pickup 3 is still seeking the address, output of digital audio data A0 100 is completed. The controller 16 therefore scans the selection specification data and recognizes a selection A specification.

The next batch of compressed data a1 111 in the buffer memory 19 is output to the expansion circuit 21 for expansion to the digital audio data A1 101. The expanded digital audio data A1 101 is then converted by the D/A converter 15 to analog audio data, and output from the output terminal 25. Reproduction of selection A thus continues.

By time t7 the optical pickup 3 has accessed the address, and therefore begins to read the next selection A data a'3 123. As described above, the read data a'3 123 is converted and stored in the buffer memory 19.

The optical pickup 3 now enters a standby state until recorded data a'4 124 can be read. At time t9 the controller 16 recognizes the selection A specification from the selection specification data, and reproduces digital audio data A2 102. The read data a'4 124 is also converted and stored in the buffer memory 19.

The optical pickup 3 enters a standby state until recorded data a'5 125 can be read. When output of digital audio data A2 102 ends at time t13, the controller 16 recognizes a change to a selection B specification from the selection specification data. It therefore outputs the compressed data b0 210 from the buffer memory 19 to the expansion circuit 21. The compressed data b0 210 is similarly expanded to digital audio data B0 200, which is converted to analog audio data by the D/A converter 15 and output from the output terminal 25.

The selection being reproduced is thus changed from selection A to selection B.

The selection A data a5 125 is nevertheless read at time t14 after the selection is changed. This results in compressed data a3 113, a4 114, and a5 115 being stored in the buffer memory 19.

At time t15 reading is completed and the optical pickup 3 begins to seek absolute address 15'01'00 to read the next block of data b3 223 for selection B. Reproduction of selection B is thus sustained for a period.

When output of digital audio data B2 202 ends at time t22, the controller 16 again recognizes a change in the selection to selection A from the selection specification data.

As a result, compressed data a3 113 is read from the buffer memory 19, expanded to the digital audio data A3 103, converted to analog audio data by the D/A converter 15, and output from the output terminal 25. Playback is therefore switched back to selection A. In addition, playback of selection A continues from the point at which selection A stopped when playback was previously changed to selection B. There is thus no interruption of the playback except when the selection is changed, and playback continues smoothly.

It is to be noted that the first and second embodiments of the invention were described above as being for the reproduction of two different selections. The invention shall not be so limited in that the reading and access of the disk 1 is a continuous operation in both of the above embodiments.

The access to the tracks of the disk 1 are pre-recorded on the disk 1. The data obtained by the optical pickup 3 is sent to the pre-recorded data detection circuit 8 through the playback amplifier 7 and a part of the data is input to the CLV control circuit 10.

The CLV control circuit 10 applies constant linear velocity (CLV) control based on the input data 1 using the photodiode 2 driving at a constant linear velocity. The data from the pre-recorded data detection circuit 8 is also input to the absolute address data track circuit 9, and the location of the optical pickup 3 is input to the controller 16. The controller 16 controls disk access based on this optical pickup 3 location information.

The method used for digital audio data compression and expansion is preferably the method described in "Standardization trends and algorithms for sound encoding" (Technical report, IT89-79, Assoc. of Electronic Information Communications of Japan), but other methods may also be used.

As described hereinabove, a data recording and reproducing apparatus according to the present invention can simultaneously reproduce plural different selections recorded to a single optical disk. In addition, the invention can also switch

without interruption between reproduction of plural different selections recorded to a single optical disk.

The invention being thus described, it will be obvious that the same may be varied in many ways. Such variations are not to be regarded as a departure from the spirit and scope of the invention, and all such modifications as would be obvious to one skilled in the art are intended to be included within the scope of the following claims.

## Claims

1. A data recording apparatus for recording input data on a recording medium in compressed data blocks comprising:
  - data dividing means (23) for dividing input data into a plurality of blocks;
  - data compression means (20) for compressing input data in each block to produce a compressed data block;
  - buffer memory means (19) for temporarily storing a predetermined number of compressed data blocks; and
  - recording control means (11, 5, 6) for recording compressed data blocks on said recording medium.
2. A data reproducing apparatus for reproducing compressed data blocks stored in a recording medium comprising:
  - reading means (3, 7, 12) for reading compressed data blocks from at least first and second areas on said recording medium, alternatively;
  - buffer memory means (19) for temporarily storing the read compressed data blocks to define a first train of compressed data blocks (110, 111, 112) and a second train of compressed data blocks (210, 211, 212); and
  - data expansion means (21, 28) for expanding at least one of said first and second trains of compressed data blocks.
3. A data reproducing apparatus as claimed in Claim 2, wherein said data expansion means comprises first and second data expansion circuits (21, 28) for simultaneously expanding said first and second trains of compressed data blocks.
4. A data reproducing apparatus as claimed in Claim 2, wherein said data expansion means comprises selection switch means (SW) for selecting one of said first and second trains of compressed data blocks and one data expansion circuit (21) for expanding the selected train of compressed data blocks.

5. A data recording/reproducing apparatus for recording input data on a recording medium in compressed data blocks and for reproducing compressed data blocks from the recording medium comprising:

data dividing means (23) for dividing input data into a plurality of blocks;

data compression means (20) for compressing input data in each block to produce a compressed data block;

buffer memory means (19) for temporarily storing a predetermined number of compressed data blocks;

recording control means (11, 5, 6) for recording compressed data blocks on said recording medium.

reading means (3, 7, 12) for reading compressed data blocks from at least first and second areas on said recording medium, alternatively;

said buffer memory means (19) for temporarily storing the read compressed data blocks to define a first train of compressed data blocks (110, 111, 112) and a second train of compressed data blocks (210, 211, 212); and

data expansion means (21, 28) for expanding at least one of said first and second trains of compressed data blocks.

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Fig. 1

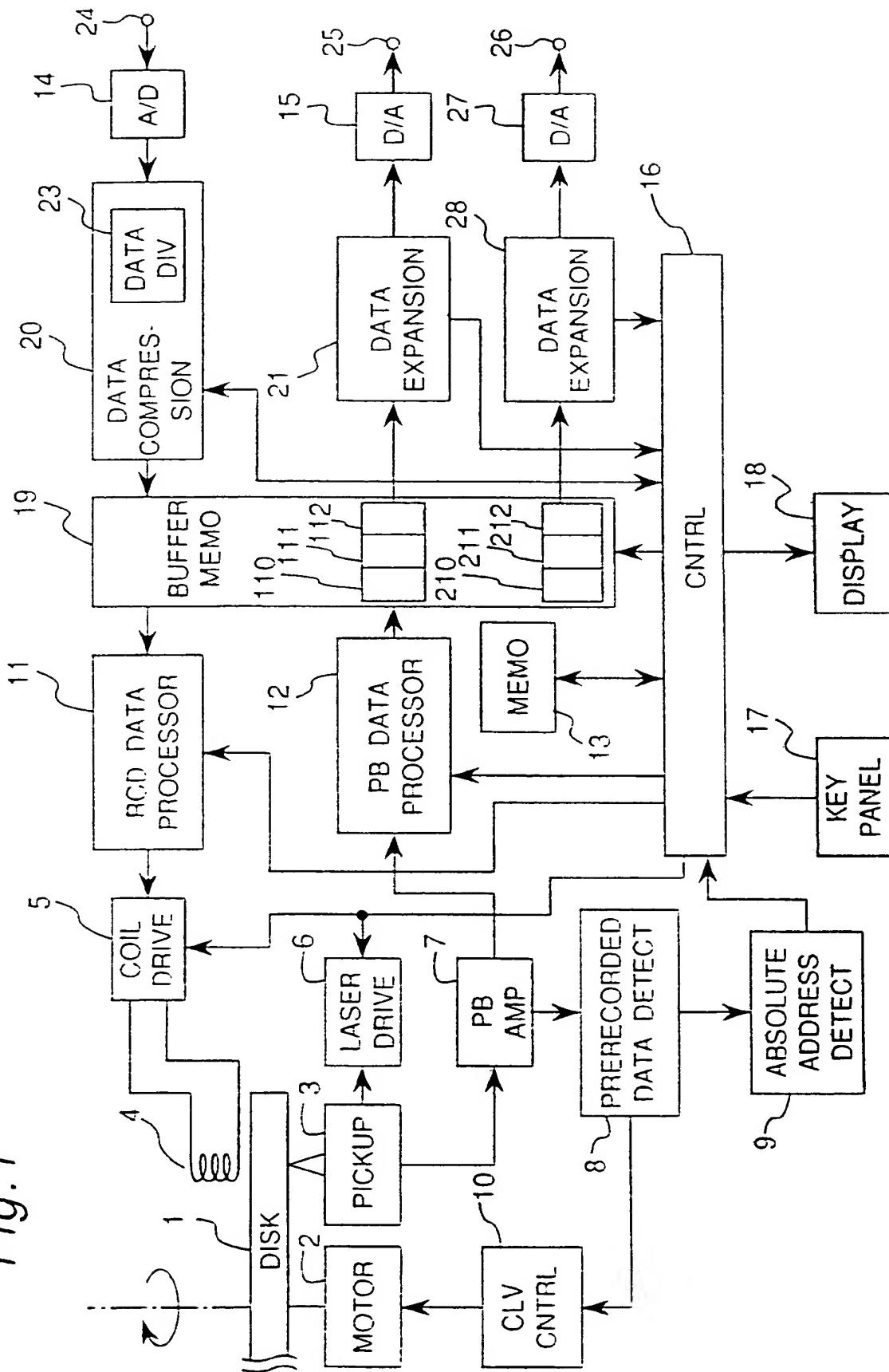




Fig.2

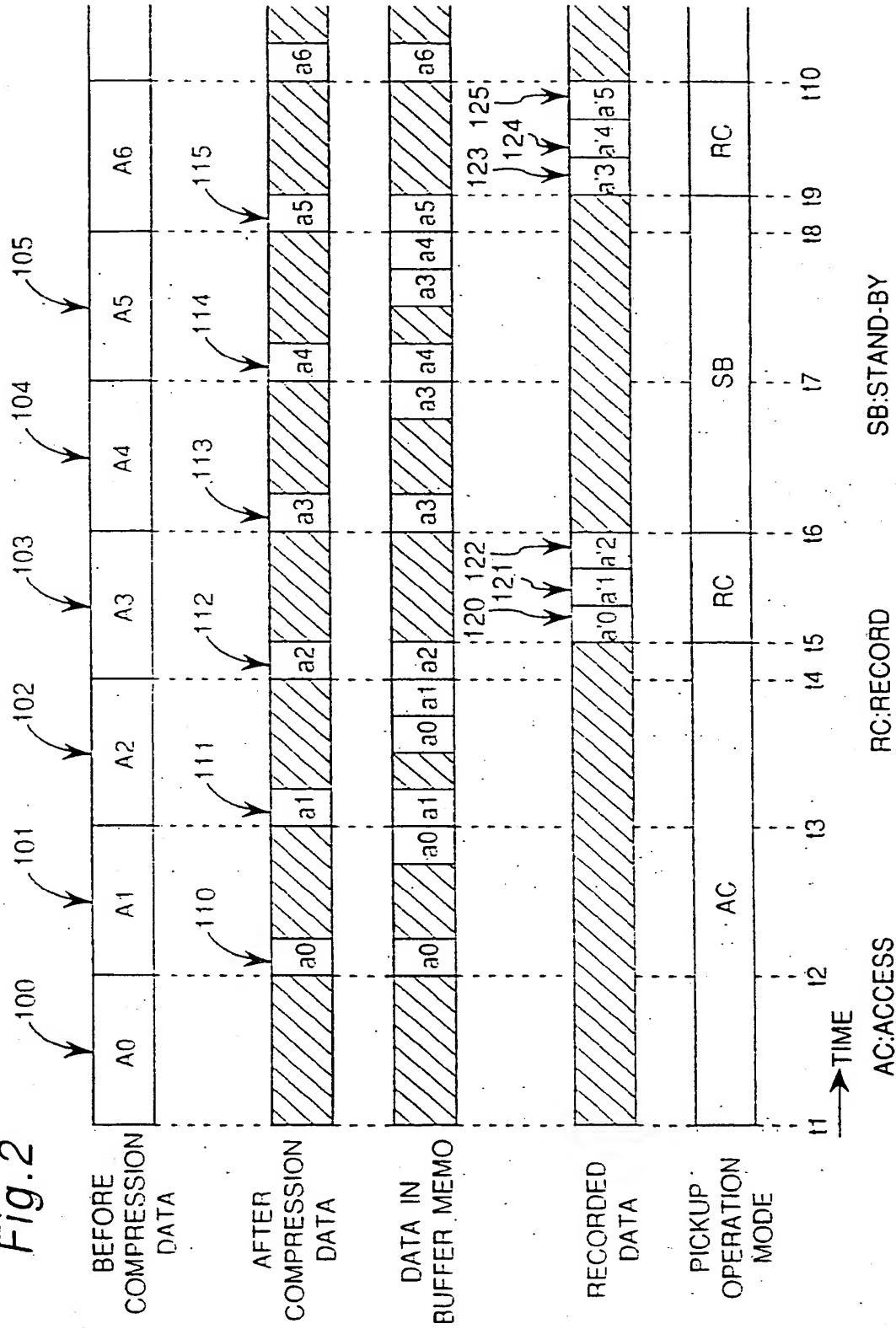
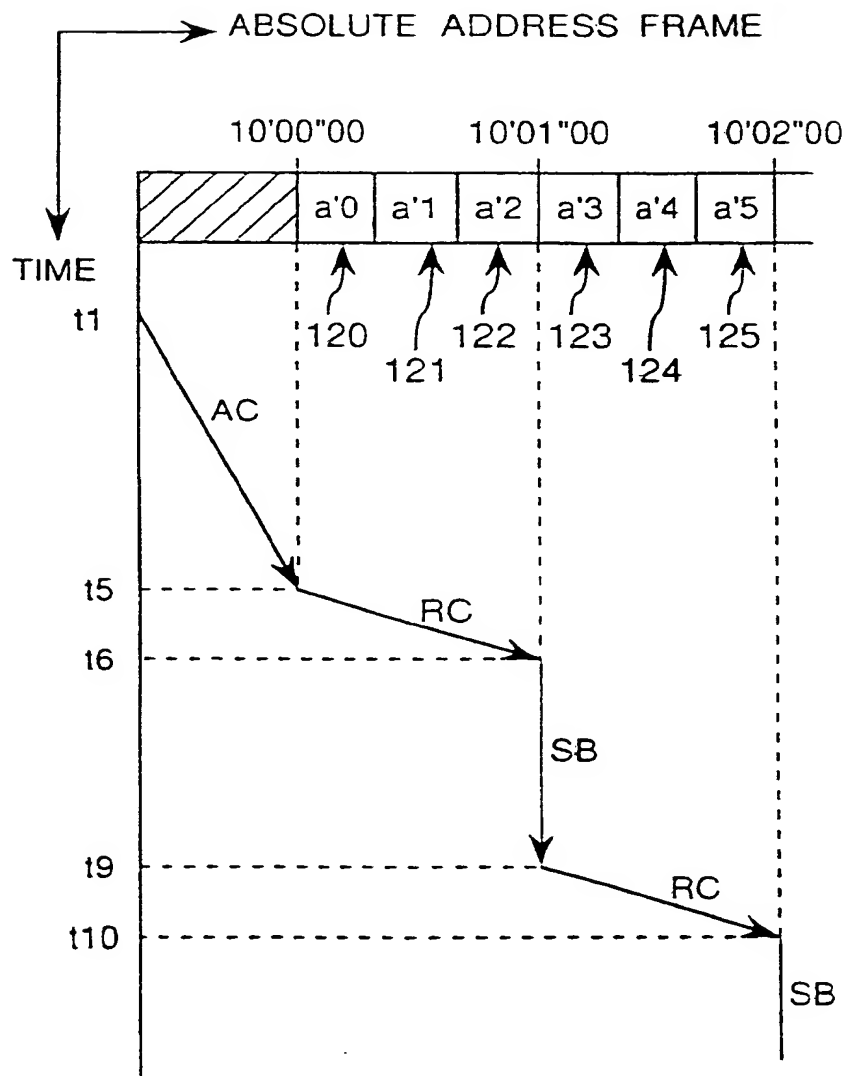


Fig.3



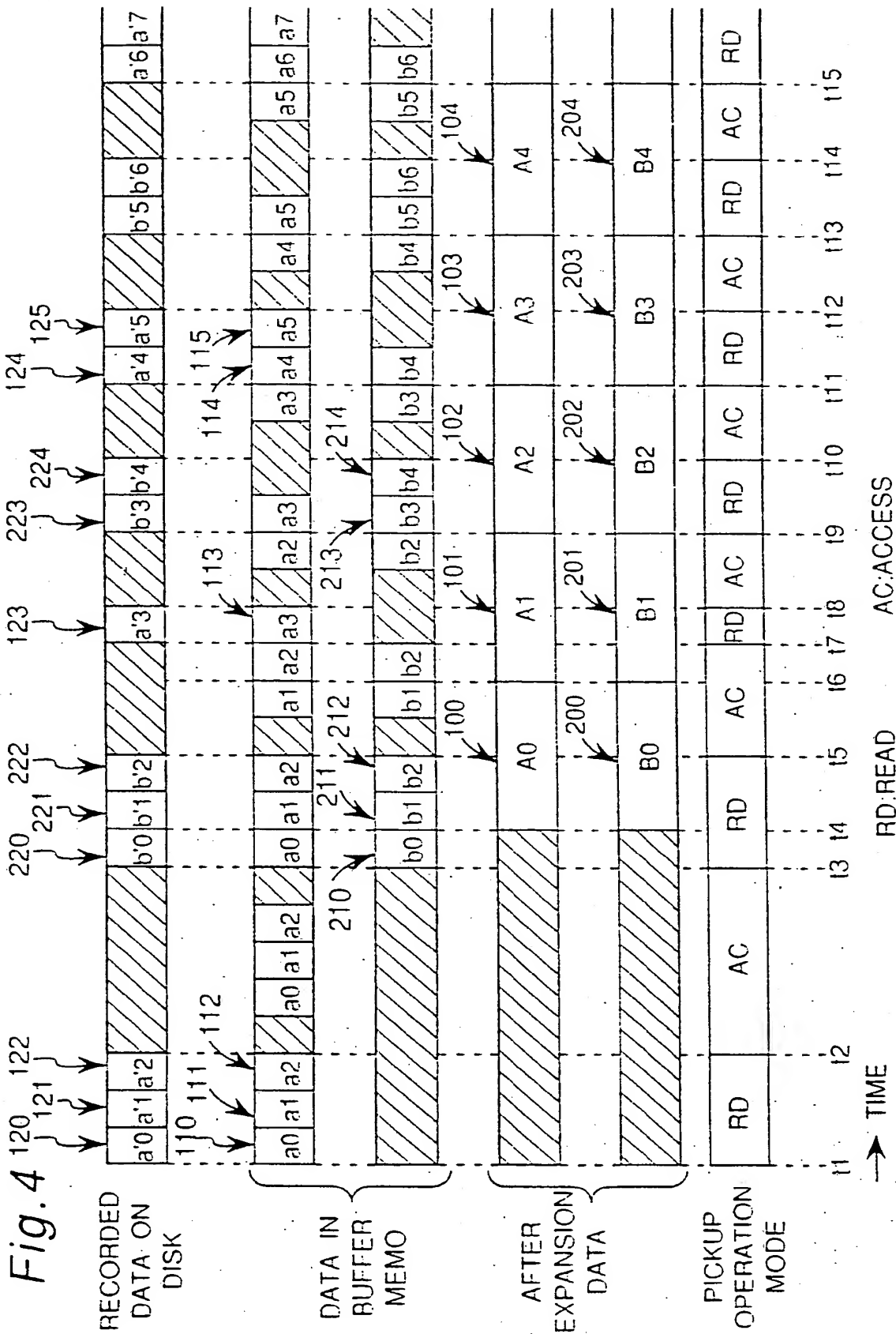


Fig. 5

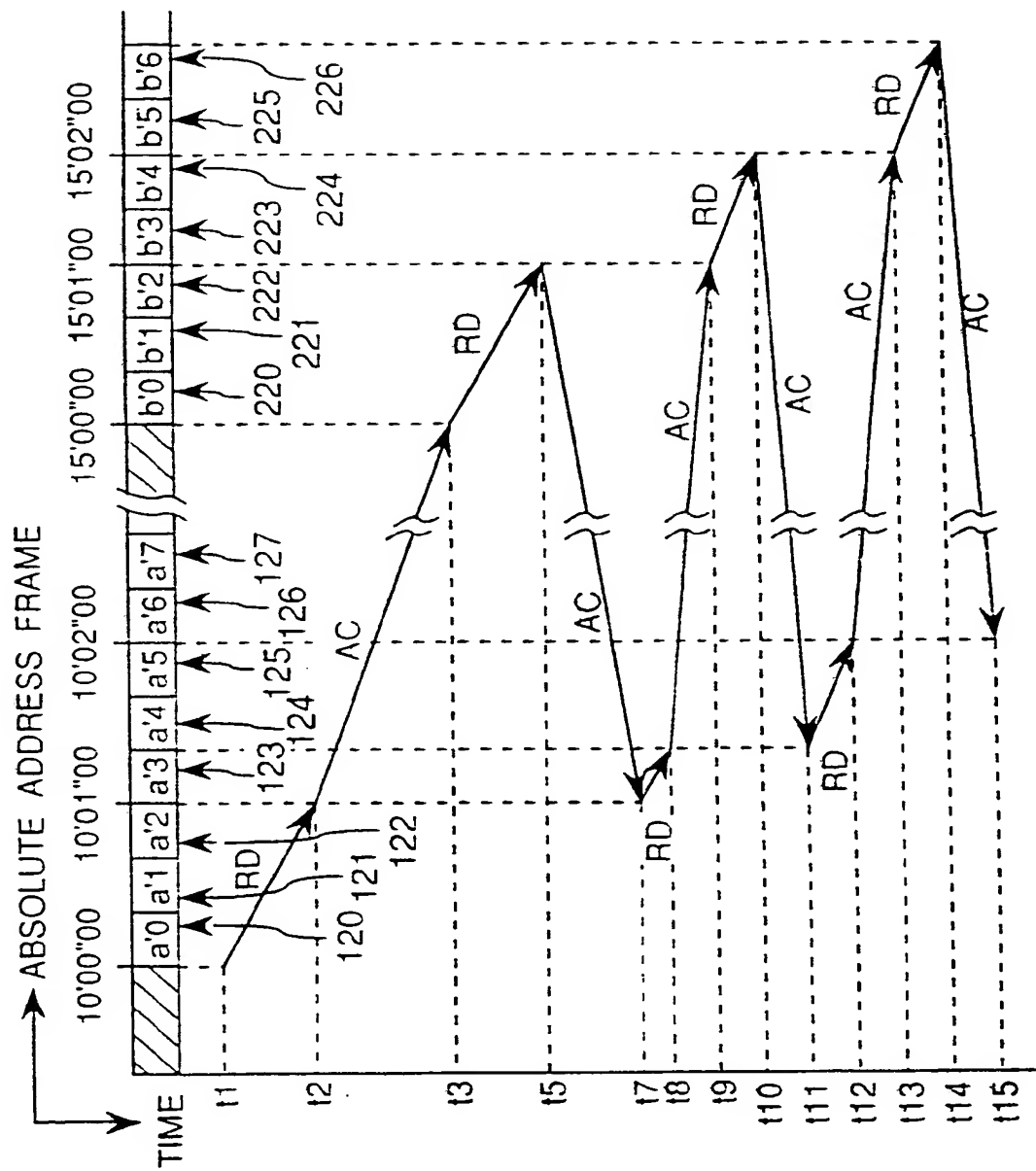


Fig. 6

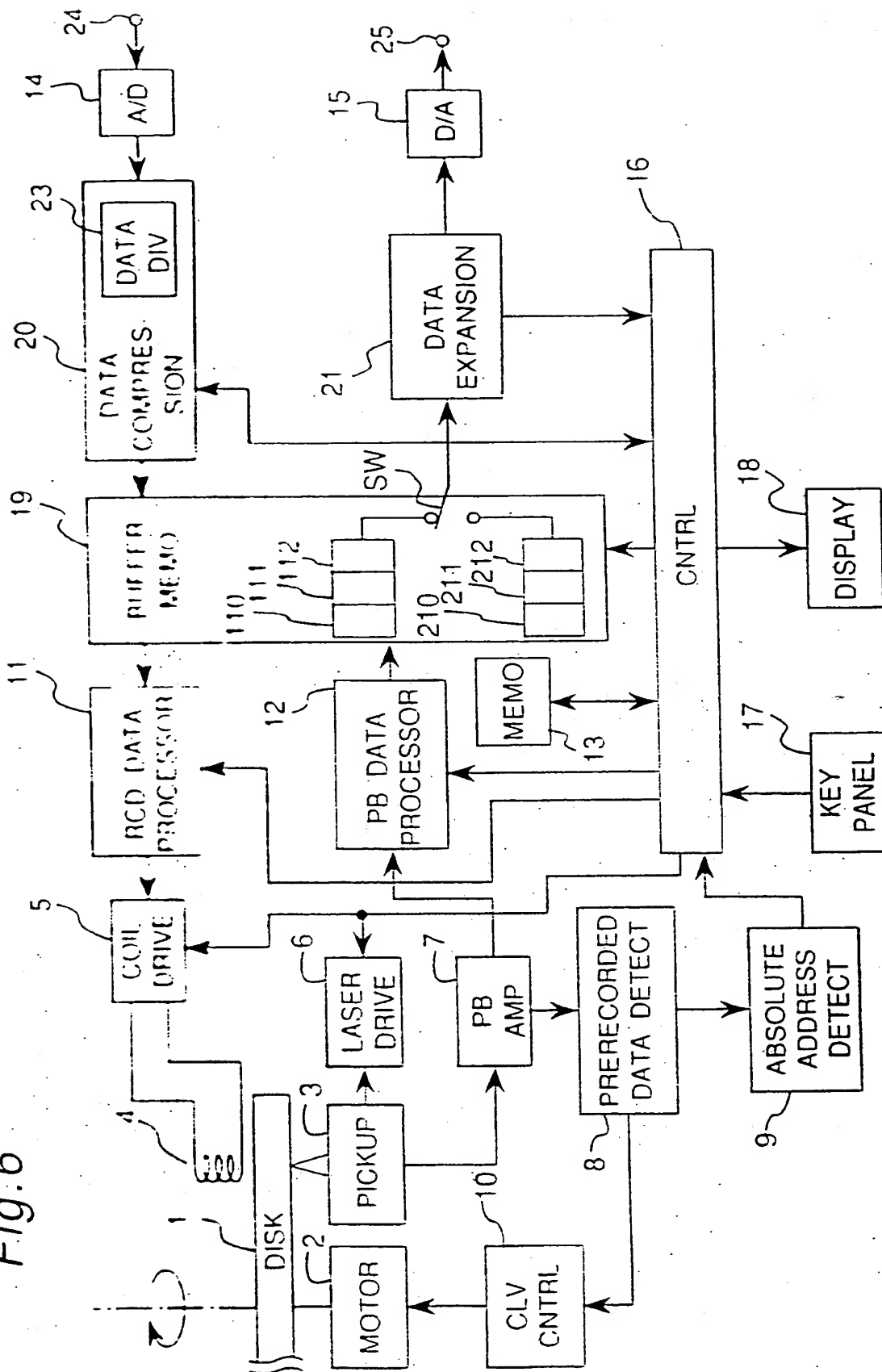


Fig. 7a

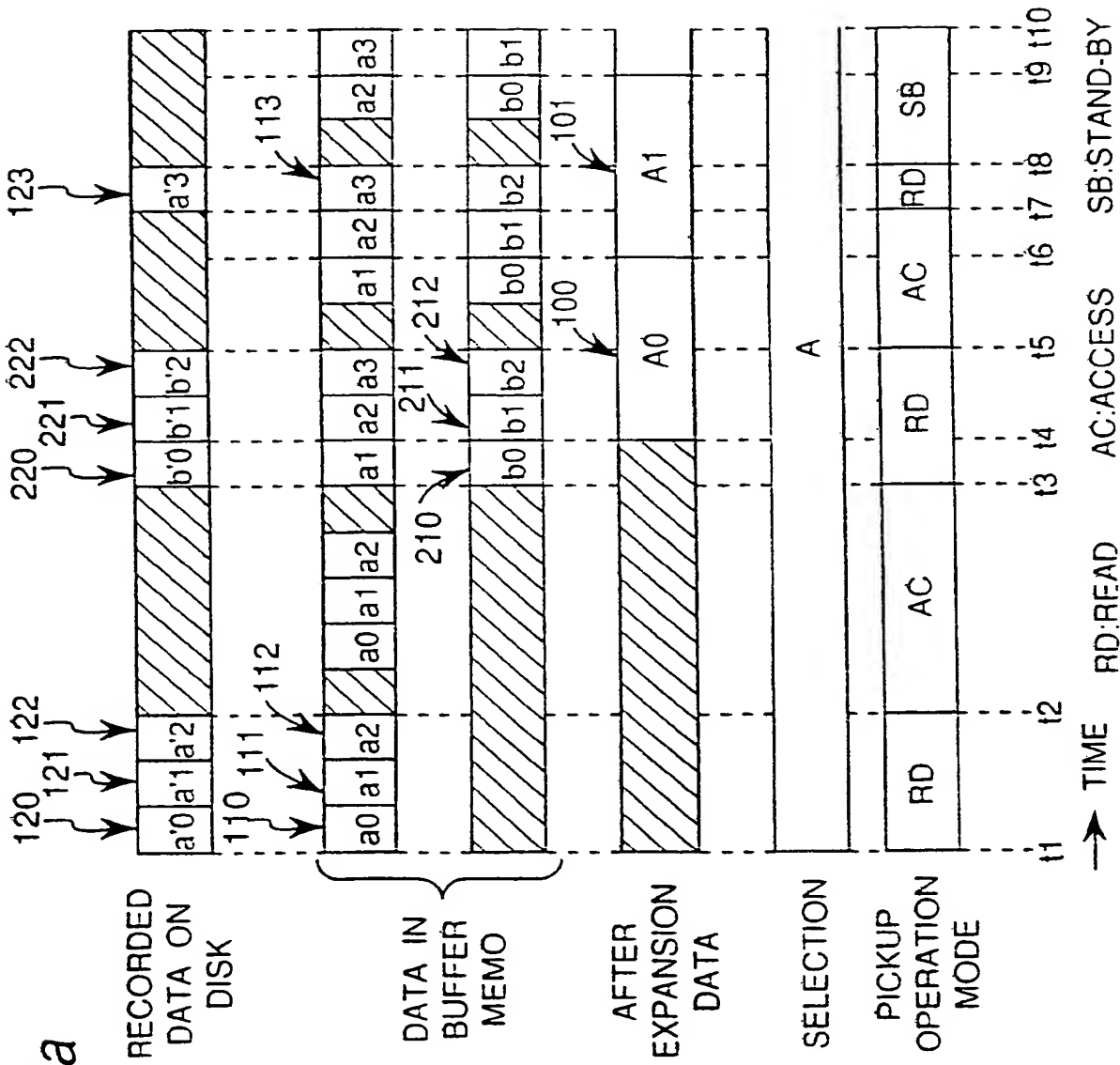


Fig. 7

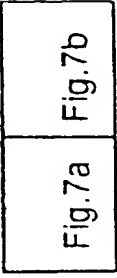
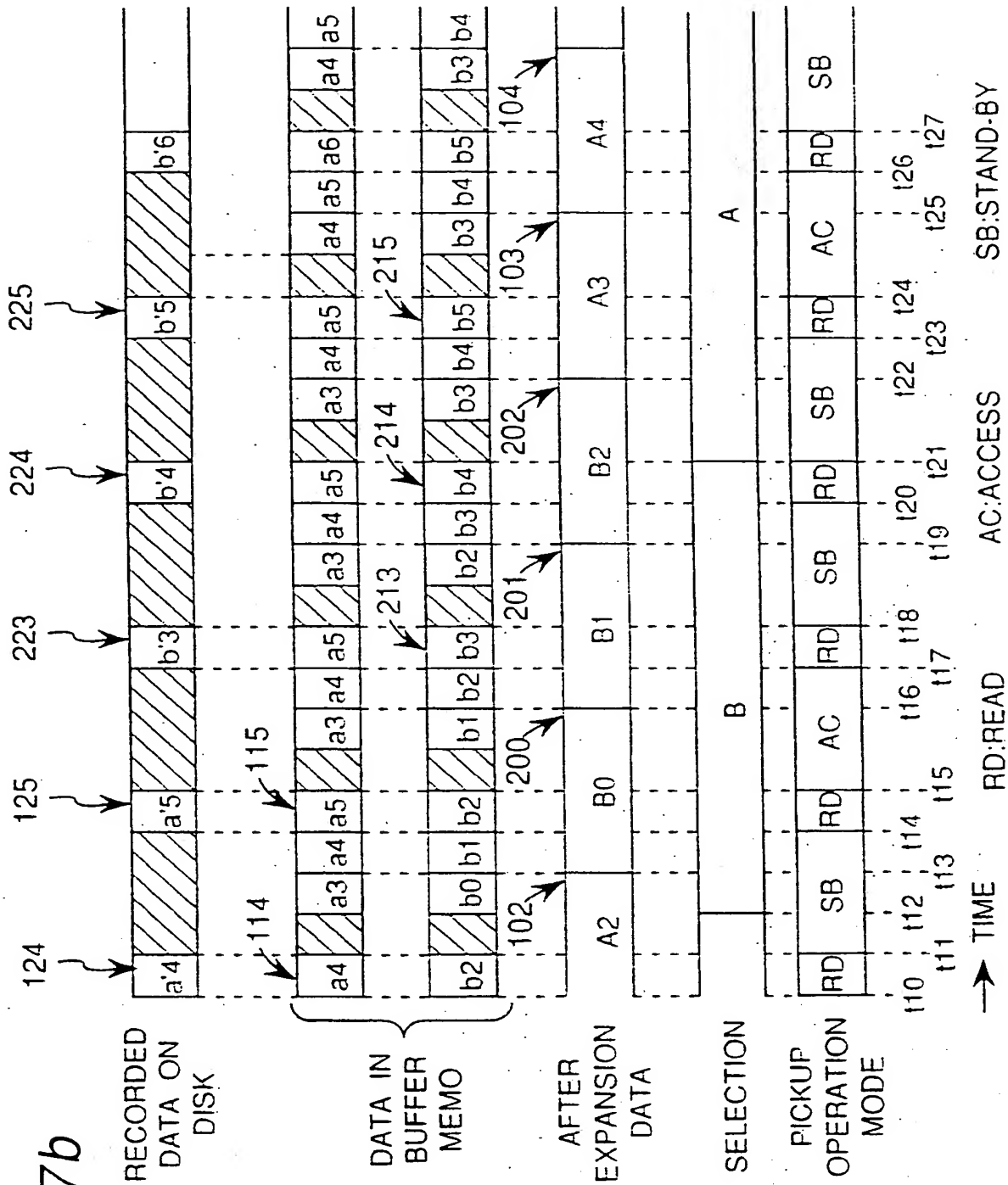


Fig. 7b



*Fig.8*

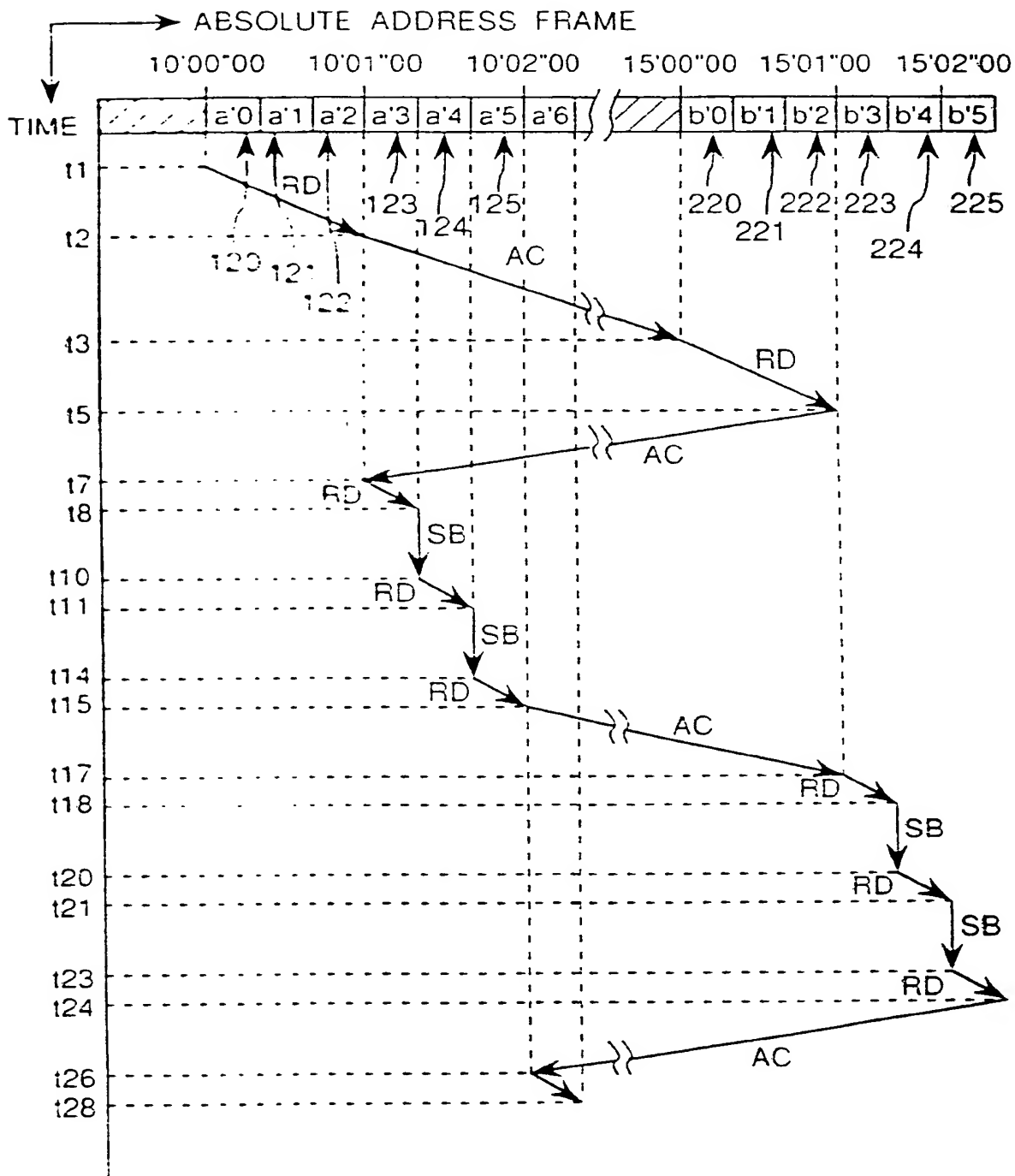
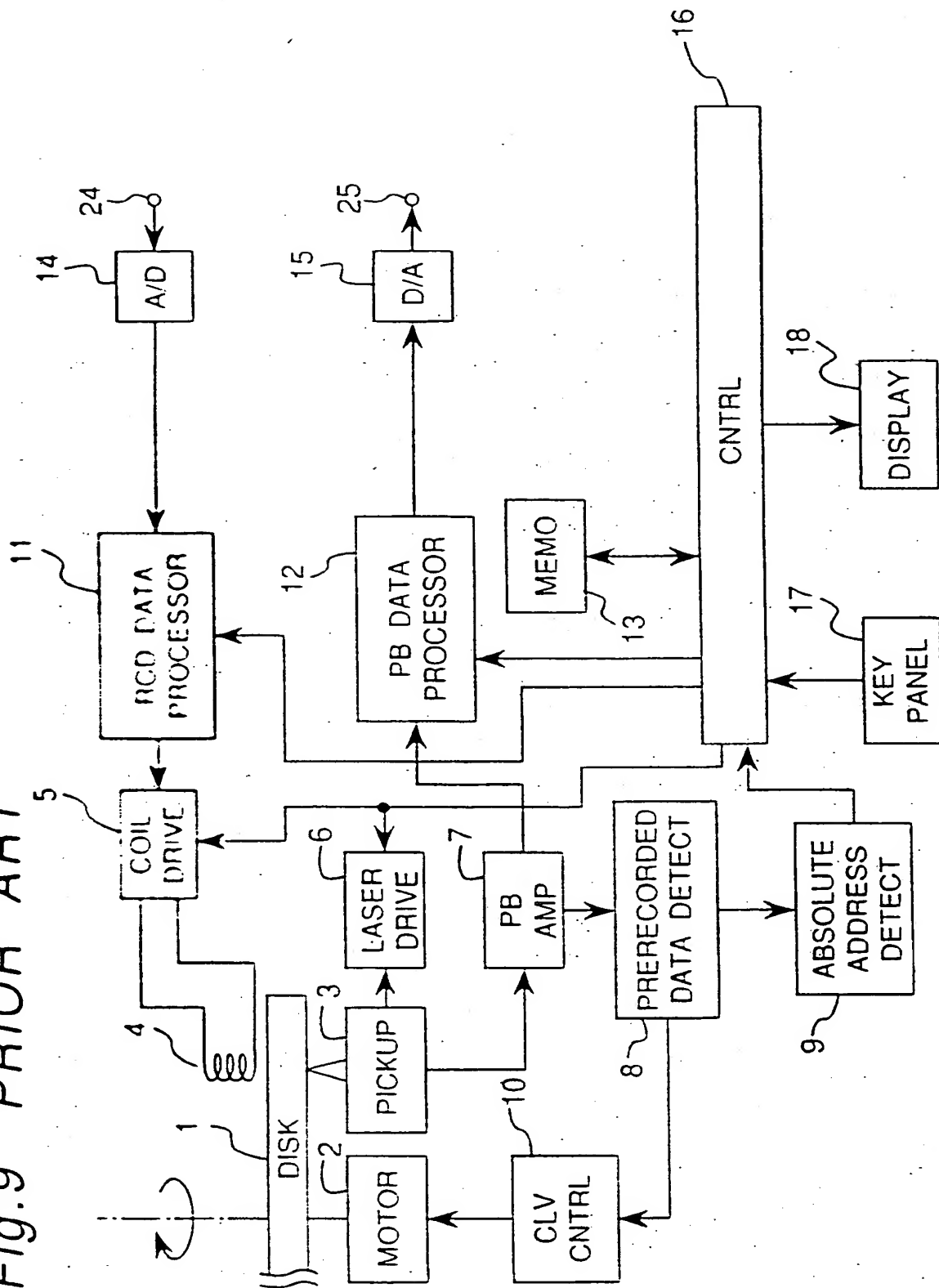




Fig.9 PRIOR ART





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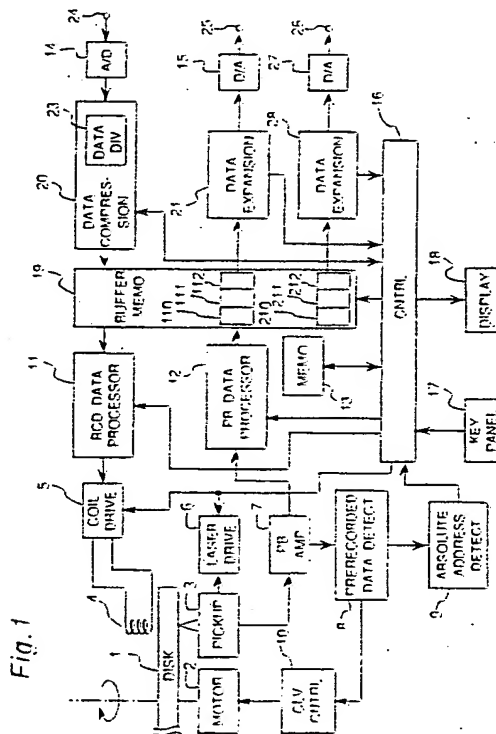
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54 Data recording and reproducing apparatus.

57 A data recording and reproducing apparatus uses a rewriteable optical disk (1) with pre-recorded absolute address information, and comprises a data compression circuit (23, 20) for dividing and compressing continuous digital data into compressed plural data blocks, an expansion circuit (21, 28) to restore the compressed data to the original expanded form, and a buffer memory (19). Data is compressed in discrete data blocks and is recorded sequentially block by block to the optical disk. The compressed data is likewise read block by block from the disk during data reproduction. Using the data expansion circuit to expand the compressed data one block at a time, the data recording and reproducing apparatus can reproduce the original continuous digital data stream. Plural continuous digital data selections can thus be reproduced by alternately fetching data for the plural selections from the disk in compressed data block units and temporarily storing the data to a buffer memory for expansion.





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## EUROPEAN SEARCH REPORT

Application Number

EP 93 10 1005

### DOCUMENTS CONSIDERED TO BE RELEVANT

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
X	US-A-4 956 808 (D.E. AAKRE ET AL) * column 1, line 54 - column 2 * ----	1	G11B20/10 G11B20/00
A	PATENT ABSTRACTS OF JAPAN vol. 10, no. 30 (P-426)(2087) 5 February 1986 & JP-A-60 179 962 ( MATSUSHITA DENKI SANGYO KK ) * abstract * ----	1	
A	EP-A-0 260 722 (TOSHIBA KK) * column 7, line 23 - column 8, line 15 * ----	2	
A	EP-A-0 463 183 (SONY CORP) * column 2 - column 4 * ----	2	
A	EP-A-0 333 706 (LUCAS FILM LTD) * page 1 - page 4 * ----	2	
A	US-A-4 506 303 (W.J.HANNAN) * column 2 * ----	1,5	TECHNICAL FIELDS SEARCHED (Int. Cl.5)
A	PATENT ABSTRACTS OF JAPAN vol. 12, no. 298 (P-744)15 August 1988 & JP-A-63 070 988 ( TOSHIBA CORP. ) * abstract * ----	2	G11B
A	PATENT ABSTRACTS OF JAPAN vol. 11, no. 107 (P-563)4 April 1987 & JP-A-61 255 536 ( MATSUSHITA ELECTRIC IND CO LTD ) * abstract * -----	2	
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 02 AUGUST 1993	Examiner SZUBERT J.M.
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